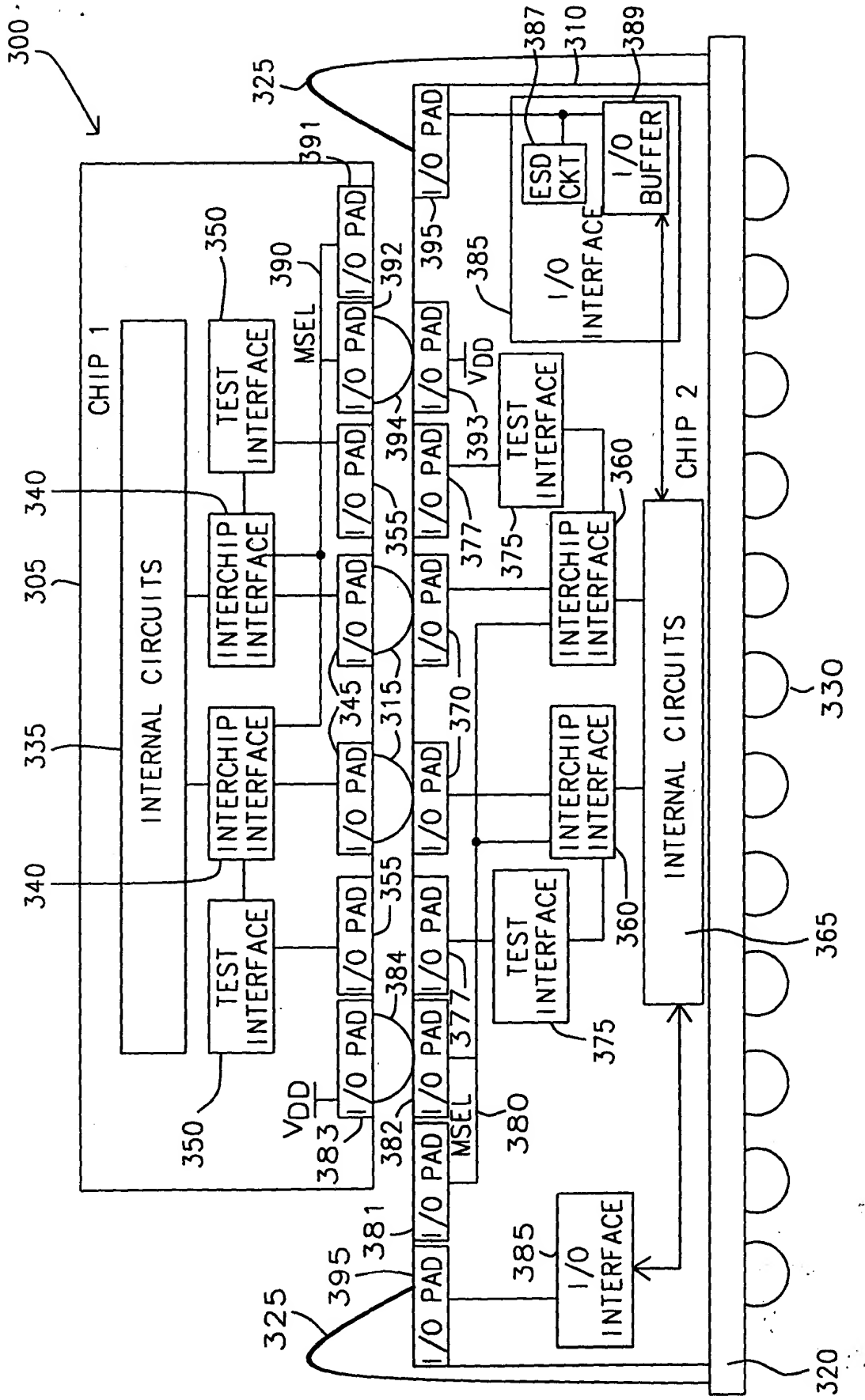
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CHIP 1
INTERNAL CIRCUITS
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TEST INTERFACE
TEST INTERFACE
INTERCHIP INTERFACE
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I/O BUFFER
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I/O INTERFACE
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TEST INTERFACE
INTERCHIP INTERFACE
INTERCHIP INTERFACE
INTERNAL CIRCUITS
CHIP 2
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320

FIG. 3